

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

SBS818 — Low VF Schottky Barrier Diode

30V, 2.0A Rectifier

Applications

· High frequency rectification (switching regulators, converters, choppers)

Features

- · Low switching noise
- Low forward voltage (IF=2.0A, VF max=0.52V)
- Ultrasmall package permitting applied sets to be small and slim (Mounting height 0.75mm)

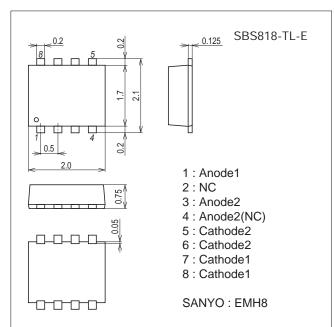
Specifications

Absolute Maximum Ratings at Ta=25°C (Value per element)

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	VRRM		30	V
Nonrepetitive Peak Reverse Surge Voltage	VRSM		30	V
Average Output Current	IO	When mounted on ceramic substrate (900mm ² ×0.8mm)	2.0	А
		When mounted on glass epoxy substrate (0.96mm ² ×0.03mm)	1.5	А
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	20	А
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Package Dimensions

unit : mm (typ) 7045-004

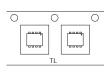


Product & Package Information

- Package
- JEITA, JEDEC

• Minimum Packing Quantity : 3,000 pcs./reel

Packing Type : TL

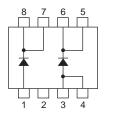




: EMH8

: -

Electrical Connection



*: Terminal 4 is used for the purposes such as test. Although it is connected to Anode 2, please handle it as NC Terminal.

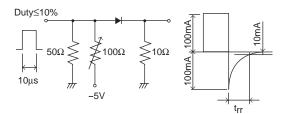
SANYO Semiconductor Co., Ltd. http://www.sanyosemi.com/en/network/

Electrical Characteristics at Ta=25°C (Value per element)

Parameter	Symbol	Conditions		Unit			
Parameter	Symbol	Conditions	min	typ	max	Unit	
Reverse Voltage	VR	IR=1mA	30			V	
	V _F 1	IF=1.0A		0.37	0.42	V	
Forward Voltage	V _F 2	IF=1.5A		0.42	0.47	V	
	V _F 3	IF=2.0A		0.46	0.52	V	
Reverse Current	IR	VR=15V			350	μΑ	
Interterminal Capacitance	С	V _R =10V, f-1MHz		30		рF	
Reverse Recovery Time	t _{rr}	IF=IR=100mA, See specified Test Circuit.			10	ns	
Thermal Resistance	Rth(j-a)1	When mounted in Cu-foiled area of 900mm ² ×0.8mm on glass epoxy substrate		100		°C / W	
	Rth(j-a)2	When mounted on ceramic substrate (900mm ² ×0.8mm)		65		°C / W	

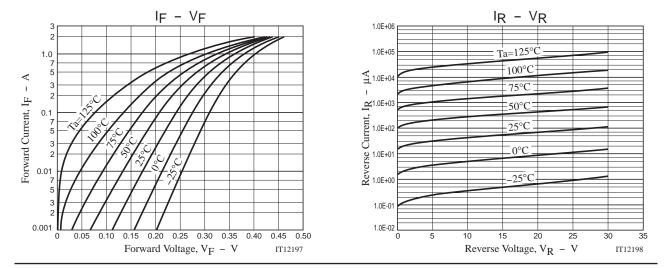
*: The absolute maximum ratings and electrical characteristics refer to those between Terminal 1 and Terminal 7 (or 8), and between Terminal 3 and Terminal 5 (or 6).

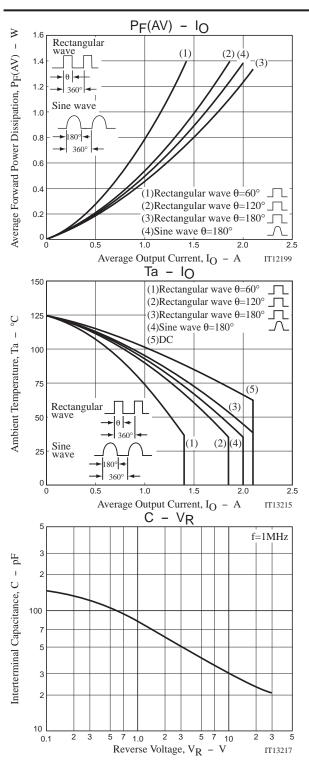
trr Test Circuit

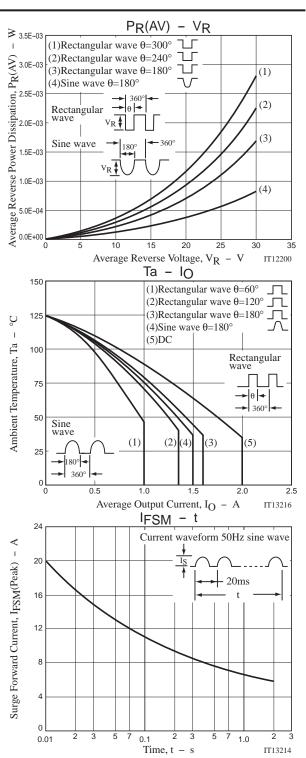


Ordering Information

Device	Device Package		memo		
SBS818-TL-E	BS818-TL-E EMH8		Pb Free		







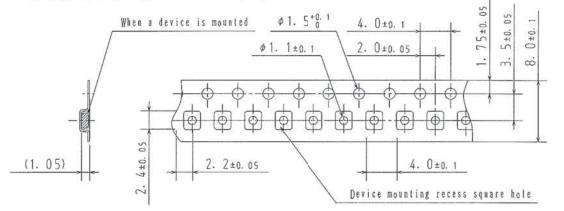
Embossed Taping Specification SBS818-TL-E

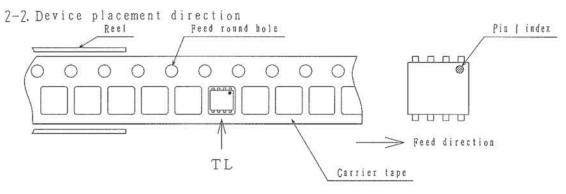
1. Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)		Packing format				
Type		Reel Inner box Outer box		Inner BOX (C-1)			Outer BOX (A-7)	
EMH8 MCP4		3, 000 15, 000		90, 000	5 reels contained Dimensions:mm (external)			6 inner boxes contained) Dimensions:mm (external)
					183×	72×	185	440×195×210
Packing met	hod			(u 1	nner box nit:mm) 59		It is The fo	r box label a label at the time of factory shipme rm of a label may change in physical bution process. 108
	Type LOT Quan		-> (11	n lot oo	OOOOO IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII			TYPE CODE
	Orig	in	121	SPECIAL * 2 0 7 2 2 SSEMELY:**** (2	LOT PACKAGE SPECIAL *20722005310C* ASSEMELY:**** (DIFFUSICAL****)
	<u>Reel</u> la	bel	Th	e LEAD FI	REE & descr of the term			hat the surface ree.
				Label			Phase	
				LEAD FRE			hase 3A hase 3	

2. Taping configuration

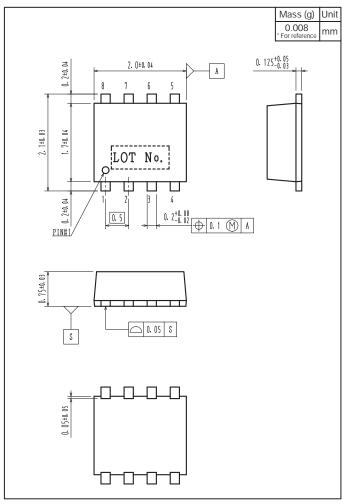
2-1. Carrier tape size (unit:mm)



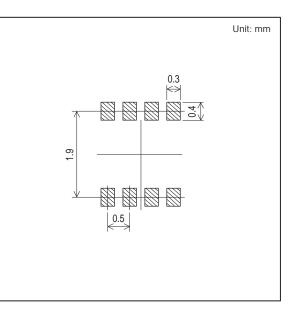


Those with pin 1 index on the feed hole side TL

Outline Drawing SBS818-TL-E



Land Pattern Example



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